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- ☐ 2. **Interfacing synchronous and asynchronous modules within a high-speed pipeline**
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- ☐ 3. **A limited-Interconnect synthetic neural IC**
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- ☐ 6. **An Efficient Field-Partition Based Code Compression and Its Pipelined Decompression Syst**
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- ☐ 10. **Matching and blending human motions temporal scaleable dynamic programming**
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- ☐ 12. **Deep pipelines vs. risk and power walls [microprocessors]**
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- ☐ **14. Channel equalisation using the non-canonical LMS**
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Term	Documents
(5 AND 6).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	4
(L6 AND L5).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	4

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<u>L13</u>	16 and 17	389	<u>L13</u>
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<u>L8</u>	11 and (user or default\$) and priorit\$7	263	<u>L8</u>
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DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L6</u>	11 and (user or default\$)	389	<u>L6</u>
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<u>L2</u>	(717/136-163)![CCLS]	5169	<u>L2</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
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